

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	47	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L2	15	array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L3	21	parady.in. and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L4	1	"4763242".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L5	7	bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L6	228	bit with reconfigur\$5 and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L7	1	"5890008".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L8	402	(status state) near2 register with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L9	61	(status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L10	5	(mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L11	13	(status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L12	57	VLIW with thread and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L13	214	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L14	403	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L15	227	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L16	652	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L17	5	(multiplexer selector) with (plurality multiple) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L18	79	(multiplexer selector) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L19	214	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L20	227	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L21	403	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L22	652	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L23	47	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L24	302	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L25	22	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L26	16	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L27	1	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L28	0	(array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L29	6	(array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L30	1	(array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L31	23	(array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L32	0	(array matrix) same (("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L33	14	(array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L34	9	(li gifford).in. and SIMD and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L35	39	bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L36	254	(array matrix) and (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L37	15	(array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L38	0	(array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L39	0	(array matrix) near4 (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L40	636	bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L41	72	bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L42	7	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did.	USPAT	OR	ON	2007/02/22 13:32
L43	0	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each	USPAT	OR	ON	2007/02/22 13:32
L44	2	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file"	USPAT	OR	ON	2007/02/22 13:32
L45	1	"4314349".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L46	905	(process task thread) same (ID identifier) same (physical and virtual)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L47	654	(process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L48	126	(process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L49	21	(process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L50	135	vliw with simd with (type form)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L51	872	instruction with virtual with (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L52	332	instruction with virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L53	97	instruction near3 virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L54	22	(ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L55	2	"virtual processor is"	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L56	22	"virtual processor" with (advantag\$3 benefi\$4)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L57	1	"6167501".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L58	1	"4763242".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L59	652	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L60	402	(status state) near2 register with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L61	47	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L62	652	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L63	227	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L64	403	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L65	214	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L66	57	VLIW with thread and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L67	13	(status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L68	302	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L69	21	parady.in. and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L70	7	bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L71	5	(mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L72	228	bit with reconfigur\$5 and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L73	227	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L74	1	"5890008".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L75	22	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L76	61	(status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L77	16	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L78	1	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L79	0	(array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L80	6	(array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L81	1	(array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L82	23	(array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L83	0	(array matrix) same (("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L84	14	(array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L85	9	(li gifford).in. and SIMD and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L86	403	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L87	39	bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L88	254	(array matrix) and (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L89	15	(array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L90	0	(array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L91	0	(array matrix) near4 (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L92	636	bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L93	214	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2007/02/22 13:32
L94	72	bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L95	7	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did.	USPAT	OR	ON	2007/02/22 13:32
L96	21	(process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L97	126	(process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L98	0	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each	USPAT	OR	ON	2007/02/22 13:32
L99	2	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file"	USPAT	OR	ON	2007/02/22 13:32
L100	1	"4314349".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32

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L101	654	(process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L102	5	(multiplexer selector) with (plurality multiple) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L103	905	(process task thread) same (ID identifier) same (physical and virtual)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L104	135	vliw with simd with (type form)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L105	872	instruction with virtual with (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L106	332	instruction with virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L107	97	instruction near3 virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L108	22	(ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L109	2	"virtual processor is"	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L110	22	"virtual processor" with (advantag\$3 benefi\$4)	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L111	1	"6167501".pn.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L112	15	array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L113	79	(multiplexer selector) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L114	47	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:32
L115	50	huisman.xa.	US-PGPUB; USPAT	OR	ON	2007/02/22 13:56
L116	352	712/229.ccls.	US-PGPUB; USPAT	OR	ON	2007/02/22 14:06
L117	36	(edwin-b\$.in. pechanek-g\$.in. strube-d\$.in.) and \$2configur\$5 with array	US-PGPUB; USPAT	OR	ON	2007/02/22 14:07
L118	11	(edwin-b\$.in. pechanek-g\$.in. strube-d\$.in.) and \$2configur\$5 with array same register	US-PGPUB; USPAT	OR	ON	2007/02/22 14:07

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L119	3	(edwin-b\$.in. pechanek-g\$.in. strube-d\$.in.) and \$2configur\$5 with array same register same context	US-PGPUB; USPAT	OR	ON	2007/02/22 14:08
L120	4	(\$2configur\$5 with array same register same context).clm.	US-PGPUB	OR	ON	2007/02/22 14:10
L122	1	(\$2configur\$5 with array same register same context)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/22 14:10
L123	11	(\$2configur\$5 with array same context)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/22 14:11
S1	28	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2004/06/24 09:30
S2	7	array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2004/06/23 16:29
S3	19	parady.in. and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/23 16:38
S4	1	"4763242".pn.	US-PGPUB; USPAT	OR	ON	2004/06/23 16:38
S5	1	bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2004/06/24 09:38
S6	154	bit with reconfigur\$5 and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2004/06/24 10:19
S7	1	"5890008".pn.	US-PGPUB; USPAT	OR	ON	2004/06/24 10:22
S8	317	(status state) near2 register with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/24 10:23
S9	47	(status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/24 13:21
S10	4	(mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/24 10:44
S11	11	(status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/24 16:05
S12	29	VLIW with thread and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2004/06/25 10:16
S13	130	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2004/06/25 10:26

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S14	324	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2004/06/25 10:26
S15	177	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2004/06/25 10:27
S16	446	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2004/06/25 10:30
S17	2	(multiplexer selector) with (plurality multiple) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2004/06/25 10:33
S18	45	(multiplexer selector) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2004/06/25 10:34
S19	145	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:56
S20	332	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:56
S21	186	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:56
S22	482	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2004/11/17 09:56
S23	39	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:05
S24	240	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3	US-PGPUB; USPAT	OR	ON	2006/03/10 13:06
S25	18	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column)	US-PGPUB; USPAT	OR	ON	2006/03/10 13:09
S26	14	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:14
S27	1	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:15
S28	0	(array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:15
S29	3	(array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:20
S30	1	(array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/10 13:21

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S31	23	(array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 08:36
S32	0	(array matrix) same ((("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 08:38
S33	12	(array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 09:20
S34	9	(li gifford).in. and SIMD and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 09:25
S35	39	bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:10
S36	240	(array matrix) and (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:10
S37	13	(array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:11
S38	0	(array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:18
S39	0	(array matrix) near4 (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:12
S40	580	bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 10:20
S41	67	bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/03/13 15:21
S42	7	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did.	USPAT	OR	ON	2006/03/13 11:38
S43	0	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each	USPAT	OR	ON	2006/03/13 11:39
S44	2	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file"	USPAT	OR	ON	2006/03/13 11:39
S45	1	"4314349".pn.	US-PGPUB; USPAT	OR	ON	2006/03/14 10:19
S46	737	(process task thread) same (ID identifier) same (physical and virtual)	US-PGPUB; USPAT	OR	ON	2006/03/14 10:32

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S47	539	(process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/03/14 10:32
S48	88	(process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/03/14 10:34
S49	16	(process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/03/14 16:39
S50	92	vliw with simd with (type form)	US-PGPUB; USPAT	OR	ON	2006/03/14 16:31
S51	728	instruction with virtual with (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/03/14 16:32
S52	284	instruction with virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/03/14 16:32
S53	87	instruction near3 virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/03/14 16:32
S54	21	(ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/03/15 09:23
S55	2	"virtual processor is"	US-PGPUB; USPAT	OR	ON	2006/03/15 09:24
S56	18	"virtual processor" with (advantag\$3 benefi\$4)	US-PGPUB; USPAT	OR	ON	2006/03/15 09:28
S57	1	"6167501".pn.	US-PGPUB; USPAT	OR	ON	2006/03/15 09:28
S58	44	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S59	14	array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S60	21	parady.in. and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S61	1	"4763242".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S62	5	bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S63	221	bit with reconfigur\$5 and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S64	1	"5890008".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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S65	392	(status state) near2 register with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S66	60	(status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S67	5	(mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S68	13	(status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S69	52	VLIW with thread and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S70	205	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S71	396	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S72	220	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S73	623	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S74	4	(multiplexer selector) with (plurality multiple) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S75	72	(multiplexer selector) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S76	205	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S77	220	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S78	396	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S79	623	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
S80	44	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S81	277	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S82	19	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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S83	16	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S84	1	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S85	0	(array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S86	5	(array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S87	1	(array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S88	23	(array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S89	0	(array matrix) same (("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S90	14	(array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S91	9	(li gifford).in. and SIMD and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S92	39	bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S93	248	(array matrix) and (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S94	14	(array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S95	0	(array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S96	0	(array matrix) near4 (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S97	617	bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S98	69	bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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S99	7	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did.	USPAT	OR	ON	2006/10/06 14:15
S100	0	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each	USPAT	OR	ON	2006/10/06 14:15
S101	2	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file"	USPAT	OR	ON	2006/10/06 14:15
S102	1	"4314349".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S103	851	(process task thread) same (ID identifier) same (physical and virtual)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S104	618	(process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S105	118	(process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S106	20	(process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S107	117	vliw with simd with (type form)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S108	813	instruction with virtual with (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S109	310	instruction with virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S110	90	instruction near3 virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S111	22	(ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S112	2	"virtual processor is"	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S113	19	"virtual processor" with (advantag\$3 benefi\$4)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
S114	1	"6167501".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15